

FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

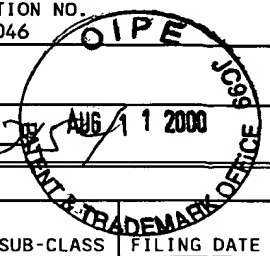
ATTY. DOCKET NO.
0056.10US

APPLICATION NO.
09/223,046

APPLICANT
Hsu et al.

FILING DATE
December 30, 1998

GROUP
2783



U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
7	AA1	3,654,621	04/1972	Bock et al.	340	172.5	
	AB1	3,916,388	10/1975	Shimp et al.	340	172.5	
	AC1	4,023,023	05/1977	Bourrez et al.	235	152	
	AD1	4,109,310	08/1978	England et al.	364	200	
	AE1	4,128,880	12/1978	Cray, Jr.	364	200	
	AF1	4,130,880	12/1978	Nutter	364	900	
	AG1	4,219,874	08/1980	Gusev et al.	364	200	
	AH1	4,317,170	02/1982	Wada et al.	364	200	
	AI1	4,396,982	08/1983	Wada et al.	364	200	
	AJ1	4,491,910	01/1985	Caudel et al.	364	200	
	AK1	4,507,731	03/1985	Morrison	364	200	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL1						Yes No
	AM1						Yes No
	AN1						Yes No
	AO1						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

7	AP	1	Halfhill, Tom R. and Belgard, Rich., "Mips vs. Lexra: Definitely Not Aligned," <i>Microprocessor Report</i> , Vol. 13, No. 16, December 6, 1999, pp. 14-19.
B	AQ	1	Hansen, Craig, "Architecture of a Broadband Mediaprocessor," <i>COMPCON96</i> , February 25-29, 1996, 8 pages.
7	AR	1	Yarlagadda, Krishna, "Lexra Adds DSP Extensions," <i>Microprocessor Report</i> , August 23, 1999, pp. 19-21.
7	AS	1	Lexra Command Line User Environment (CLUE) for ASYM-LX User's Guide, Revision 1.1, January 28, 2000, pp. 1-32.

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LARRY D. DONAGHUE
PRIMARY EXAMINER

DATE CONSIDERED

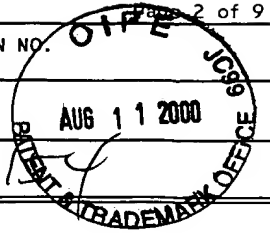
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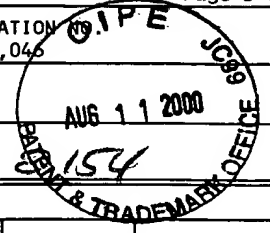
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J	AA2	4,520,439	05/1985	Liepa	364	200	
	AB2	4,583,199	04/1986	Boothroyd <i>et al.</i>	364	900	
	AC2	4,773,006	09/1988	Kinoshita <i>et al.</i>	364	200	
	AD2	4,814,976	03/1989	Hansen <i>et al.</i>	364	200	
	AE2	5,513,366	04/1996	Agarwal <i>et al.</i>	395	800	
	AF2	5,590,345	12/1996	Barker <i>et al.</i>	395	800	
	AG2	5,666,298	09/1997	Peleg <i>et al.</i>	364	715.08	
	AH2	5,721,892	02/1998	Peleg <i>et al.</i>	395	562	
	AI2	5,734,874	03/1998	Van Hook <i>et al.</i>	395	513	
	AJ2	5,740,340	04/1998	Purcell <i>et al.</i>	395	118	
	AK2	5,761,523	06/1998	Wilkinson <i>et al.</i>	395	800.2	

FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL2						Yes No
	AM2						Yes No
	AN2						Yes No
	AO2						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)			
AP	2	LX4280 (Copyright 1998-1999) < http://www.lexra.com/lx4280.html >, 2 pages.	
AQ	2	LX5280 (Copyright 1998-1999) < http://www.lexra.com/lx5280.html >, 2 pages.	
AR	2	LX4080P (Copyright 1998-1999) < http://www.lexra.com/lx4080p.html >, 6 pages.	
AS	2	LX4080 (Copyright 1998-1999) < http://www.lexra.com/lx4080.html >, 8 pages.	

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U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
9	AA3	5,812,147	09/1998	Van Hook <i>et al.</i>	345	511	
	AB3	5,815,723	09/1998	Wilkinson <i>et al.</i>	395	800.2	
	AC3	5,881,307	03/1999	Park <i>et al.</i>	395	800.23	02/24/1997
	AD3	5,922,066	07/1999	Cho <i>et al.</i>	712	204	02/24/1997
	AE3	5,933,650	08/1999	van Hook <i>et al.</i>	395	800.02	10/09/1997
	AF3	5,936,872	08/1999	Fischer <i>et al.</i>	364	754.03	12/20/1995
	AG3	5,960,012	09/1999	Spracklen	371	53	06/23/1997
	AH3	5,996,056	11/1999	Volkonsky	712	1	06/24/1997
	AI3	6,006,316	12/1999	Dinkjian	712	22	12/20/1996
	AJ3	6,058,465	05/2000	Nguyen	712	7	08/19/1996
	AK3						

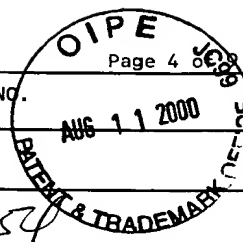
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	AM3						Yes No
	AN3						Yes No
	AO3						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)			
9	AP	3	LX4180 (Copyright 1998-1999) < http://www.lexra.com/lx4180.html >, 9 pages.
9	AQ	3	Lexra [Products] (Copyright 1998-1999) < http://www.lexra.com/lx_products2.html >, 2 pages.
9	AR	3	"Lexra Rolls Out Second MIPS Core. (Lexra LX4180) (Product Announcement)," <i>Microprocessor Report</i> , January 28, 1999, 2 pages (printed from West database).
9	AS	3	"LX4280 Fills Lexra's Midrange. (MIPS-compatible embedded processor core) (Product Announcement)," <i>Microprocessor Report</i> , August 2, 1999, 2 pages (printed from West database).

EXAMINER LARRY D. DONAGHUE PRIMARY EXAMINER	DATE CONSIDERED 29/08/00
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA4						
	AB4						
	AC4						
	AD4						
	AE4						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL4						Yes No
	AM4						Yes No
	AN4						Yes No
	AO4						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AP	4	Lexra Announces LX4080 Embedded RISC Processor Core. Delivering High Performance & executes MIPS-I instructions set* (January 12, 1998) < http://www.lexra.com/pr_980112.html >, 2 pages.
	AQ	4	Lexra Announces LX4080 SmoothCore™ Silicon Proven RISC Core (March 23, 1998) < http://www.lexra.com/pr_980323.html >, 3 pages.
	AR	4	Lexra Announces its LX4080 Evaluation System is Proven and Available for Production (July 20, 1998) < http://www.lexra.com/pr_980720.html >, 2 pages.
	AS	4	Lexra Announces the First Mainstream 32-bit RISC Core In a Programmable Logic Device (October 5, 1998) < http://www.lexra.com/pr_981005.html >, 2 pages.

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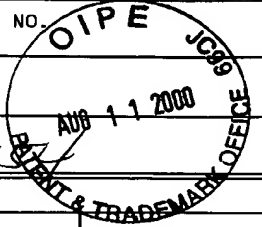
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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA5						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL5						Yes No
	AM5						Yes No
	AN5						Yes No
	AO5						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AP	<u>5</u>	Lexra Announces Next Generation Embedded RISC Core (January 11, 1999) < http://www.lexra.com/pr_990111.html >, 3 pages.
	AQ	<u>5</u>	Lexra Announces Industry's First RISC-DSP Core Based on MIPS® Instruction Set Architecture (May 5, 1999) < http://www.lexra.com/pr_990505.html >, 3 pages.
	AR	<u>5</u>	Lexra Announces the Fastest 32-BIT RISC Core to Execute MIPS® Instructions* (July 19, 1999) < http://www.lexra.com/pr_990719.html >, 2 pages.
	AS	<u>5</u>	MIPS Technologies, Inc. Files Lawsuit to Protect Intellectual Property Rights (October 28, 1999) < http://www.mips.com/pressReleases/102899B.html >, 2 pages.

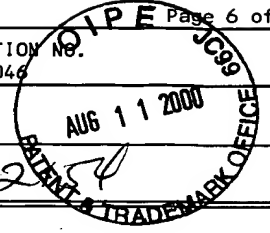
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U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA6						
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FOREIGN PATENT DOCUMENTS							
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	AL6						Yes No
	AM6						Yes No
	AN6						Yes No
	AO6						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)			
	AP	6	<p>MIPS: Open Letter from John Bourgojn, CEO, MIPS Technologies (visited February 24, 2000) <http://www.mips.com/pressReleases/102899C.html>, 2 pages.</p>
	AQ	6	<p>MIPS Technologies, Inc. Litigation Overview and Q&A (visited February 24, 2000) <http://www.mips.com/pressReleases/102899D.html>, 3 pages.</p>
	AR	6	<p>Lexra ASYM-LX Simulator/FIR Filter Demo, Date unknown, 5 pages.</p>
	AS	6	<p>Lexra Asym-LX Instruction Set Simulator Product Brief, Date unknown, 2 pages.</p>

<p>EXAMINER</p> <p style="text-align: center;">LARRY D. DONAGHUE PRIMARY EXAMINER</p>	<p>DATE CONSIDERED</p> <p style="text-align: center;">09/06/00</p>
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APPLICATION NO.
09/223,046

APPLICANT
Hsu et al.

FILING DATE
December 30, 1998

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2785

AUG 11 2000



U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA7					
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL7					Yes No
	AM7					Yes No
	AN7					Yes No
	A07					Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AP	Z	Hays, Pat, LX5280 High-Performance RISC-DSP for IP Licensing (June 8, 1999) < http://www.lexra.com/presentation/ >, 27 pages.
	AQ	Z	Lexra LX5280-Seminar-Slides; Date-unknown, 49 pages.
	AS	Z	88410 Second Level Cache, Microprocessor Forum, November 1991, 20 pages.

EXAMINER

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8/108/00

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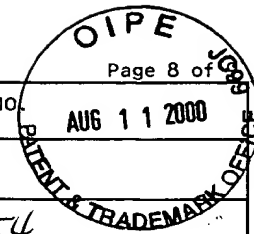
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA8						
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FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL8						Yes No
	AM8						Yes No
	AN8						Yes No
	AO8						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AP	<u>8</u>	Eyre, Jennifer and Bier, Jeff, "Infineon's TriCore Tackles DSP," <i>Microprocessor Report</i> , April 19, 1999, pp. 12-14.
	AQ	<u>8</u>	Case, Brian, "Superscalar Techniques: SuperSPARC vs. 88110," <i>Microprocessor Report</i> , Vol. 5, No. 22, December 4, 1991, pp. 1 and 6-11.
	AR	<u>8</u>	Feigel, Curtis P., "TI Introduces Four-Processor DSP Chip," <i>Microprocessor Report</i> , March 28, 1994, pp. 22-25.
	AS	<u>8</u>	Bier, Jeff, "DSP16xxx Targets Communications Apps," <i>Microprocessor Report</i> , September 15, 1997, pp. 11-15.

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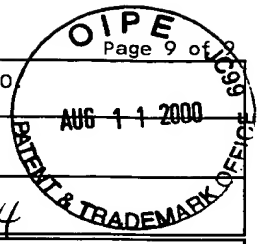
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	APPLICANT Hsu <i>et al.</i>	
	FILING DATE December 30, 1998	GROUP 2783-2154

U.S. PATENT DOCUMENTS							
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FOREIGN PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AL9						Yes No
	AM9						Yes No
	AN9						Yes No
	AO9						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)			
	AP	2	Turley, Jim and Hakkarainen, Harri, "TI's New C6x DSP Screams at 1,600 MIPS," <i>Microprocessor Report</i> , February 17, 1997, pp. 14-17.
	AQ	2	
	AR	2	
	AS	2	

EXAMINER LARRY D. DONAGHIE <small>PRIMA</small>	DATE CONSIDERED <i>6/23/00</i>
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